EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	1251	tcl	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2002/09/06 15:34
S2	1112	verilog	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2002/08/22 15:34
S3	18	verilog and tcl	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2002/08/23 07:19
S4	70	tcl/tk	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2002/08/23 07:19
S5	20	verilog and (tcl or tcl/tk)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2002/08/23 07:57
S6	1	2001-602651.NRAN.	DERWENT	OR	OFF	2002/08/23 07:45
S7	5	("5396435" | "5459673" | "5463563" | "5502661" | "5544066").PN.	USPAT	OR	ON	2002/08/23 07:46
S8	19	"5696771".URPN.	USPAT	OR	ON	2002/08/23 07:46
S9	87	verilog and script$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2002/08/23 07:58
S10	156	(VHDL or HDL) and script$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2002/08/23 07:59
S11	95	("CADENCE DESIGN SYSTEMS- INC. " "CADENCE DESIGN SYSTEM- INC. ").as.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2002/08/23 13:08

EAST Search History

S12	0	port$3 near5 tcl near5 hardware	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2002/08/29 13:03
S13	0	port$3 near15 tcl near15 hardware	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2002/08/29 13:03
S14	6	script$3 near5 ((design near3 verification) or (test adj bench))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2002/08/29 15:00
S15	4	("5802382" | "6041400" | "6092179" | "6237132").PN.	USPAT	OR	ON	2002/08/29 15:05
S16	13	"5600579".URPN.	USPAT	OR	ON	2002/08/29 15:09
S17	59	"MENTOR GRAPHICS CORPORATION".as.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2002/09/03 11:41
S18	13	"MENTOR GRAPHICS CORPORATION".as. and verilog	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2002/09/03 11:42
S19	0	PCI with source adj model	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2002/09/06 07:50
S20	188	PCI with model	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2002/09/06 07:51
S21	22	(PCI with model) and verilog	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2002/09/06 07:51
S22	0	verilog and MCD	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2002/09/06 15:34

S23	0	verilog and multi$channel adj descriptor	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2002/09/06 15:35
S24	0	verilog and channel adj descriptor	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2002/09/06 15:35
S25	130	channel adj descriptor	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2002/09/06 15:35
S26	2	multi$channel adj descriptor	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2002/09/06 15:36
S27	985	MCD	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2002/09/06 15:36
S28	1	MCD and hdl	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2002/09/06 15:36
S30	4	verilog with $thread$	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/13 08:50
S31	2099	tcl	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/13 15:57
S32	2948	verilog	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/13 15:57
S33	44	verilog and tcl	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/13 15:57

EAST Search History

S34	162	tcl/tk	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/13 15:57
S35	44	verilog and (tcl or tcl/tk)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/13 15:57
S36	299	verilog and script$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/13 15:58
S37	489	(VHDL or HDL) and script$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/13 15:58
S38	0	port$3 near5 tcl near5 hardware	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/13 15:58
S39	0	port$3 near15 tcl near15 hardware	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/13 15:58
S40	24	script$3 near5 ((design near3 verification) or (test adj bench))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/13 15:58
S41	1	PCI with source adj model	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/13 15:58
S42	444	PCI with model	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/13 15:58
S43	40	(PCI with model) and verilog	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/13 15:58

S44	1	verilog and MCD	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/13 15:58
S45	0	verilog and multi$channel adj descriptor	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/13 15:58
S46	1	verilog and channel adj descriptor	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/13 15:58
S47	252	channel adj descriptor	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/13 15:58
S48	2	multi$channel adj descriptor	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/13 15:58
S49	1877	MCD	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/13 15:59
S50	17	MCD and hdl	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/13 15:59
S51	116	717/115.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/13 15:59
S52	1599	716/4.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/13 15:59
S53	1197	716/5.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/13 15:59

S54	1001	716/6.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/13 15:59
S55	452	716/7.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/13 16:00
S56	486	716/17.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/13 16:00
S57	759	716/18.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/13 16:00
S58	256	712/36.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/13 16:00
S59	726	703/14.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/13 16:00
S60	333	703/15.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/13 16:00
S61	179	703/16.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/13 16:00
S62	4	vhdl with thread	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/08 13:31
S63	10	(verilog vhdl) with (multi-thread$3 multithread$3 thread)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/08 13:36

EAST Search History

S64	45	(verilog vhdl) same (multi-thread$3 multithread$3 thread)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/08 13:38
S65	9	hdl with (multi-thread$3 multithread$3 thread)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/08 14:14
S66	0	"electronic design automation" with (multi-thread$3 multithread$3 thread$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/08 14:15
S67	2	eda with (multi-thread$3 multithread$3 thread$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/08 14:15